V.P. & R.P.T.P SCIENCE COLLEGE
INTERANAL EXAM
T.Y.B.Sc.(SEM-V) ELECTRONICS

SUB: - Digital systems   SUB CODE: US05CELE02

DATE:-03/10/2019   TIME: 11:00 pm to 12:15 pm   MARKS-25

Q-1   Choose correct answer   [05]

1.   Registers are made of _____.
       (A) flip-flops   (C) Resistor
       (B) capacitor   (D) None of above
2.   In controlled buffer register _____ is the control input.
       (A) LOAD   (C) serial input
       (B) CLR   (D) None of above
3.   In bi-directional register data bit is shifted from left to right and right to ___.
       (A) left   (C) up
       (B) right   (D) None of above
4.   An IC 74LS83 is a _____ bit parallel adder.
       (A) four   (C) one
       (B) three   (D) None of above
5.   The Schmitt trigger has UTP and _____.
       (A) MTP   (C) NO- PT
       (B) LTP   (D) None of above
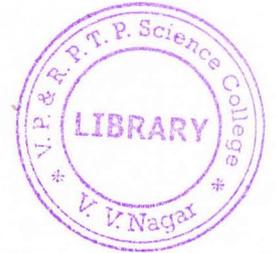
Q-2   List different types of Data transmission in registers and explain it with diagram.   [05]

                                        OR

Q-2   Draw the circuit of four bit serial shift register and explain it in detail.   [05]

Q-3   Draw the circuit of four bit bidirectional shift register and explain it in brief.   [05]

                                        OR

Q-3   Explain Tri-state switch in detail.   [05]

Q-4   Give an account of comparator.   [05]

                                        OR

Q-4   Draw a neat circuit of look ahead carry adder.   [05]

Q-5   Explain CMOS to TTL interfacing in detail.   [05]

                                        OR

Q-5   Draw the diagram of Schmitt trigger as a interface circuit and explain it.   [05]