

V.P & R.P.T.P. SCIENCE, V.V.NAGAR

B.Sc. (VIth SEM.) ELECTRONICS Internal Exam

DATE: 06/03/2019

SUB: US06CELE02

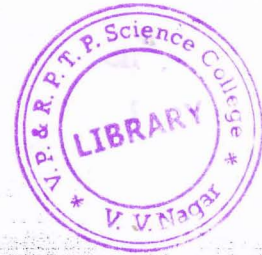
TIME: 10:00 am to 12:00 noon

MARKS-50

Q-1 Choose correct answer

[08]

1. V/F type A/D converter _____ is constant.
(A) Time (C) Frequency.
(B) Phase (D) None of these
2. Flash/Comparator type A/D converter is _____ converter.
(A) Fastest (C) Medium
(B) Slowest (D) None of these
3. Tristate switch has Low, High and _____ state.
(A) Floating (C) short circuit
(B) open (D) None of these
4. In static Bipolar RAM 0 & 1 are sensed by presence of _____.
(A) Current (C) Voltage
(B) Resistor (D) None of these
5. The control input \overline{CS} stands for _____.
(A) Chip select (C) chip stop
(B) chip chops (D) None of these
6. BJT memory is _____ than MOS memory,
(A) Faster (C) Slower
(B) Not working (D) None of these
7. V/T type A/D converter _____ is constant.
(A) Time (C) Frequency.
(B) Phase (D) None of these
8. The counter type A/D converter uses _____ counter.
(A) Up (C) Up-Down
(B) Down (D) None of above



Q-2 Short answer type question. (any Five)

[10]

1. Explain what do you understand by program memory & data memory.
2. Draw the diagram of static bipolar (TTL) RAM cell.
3. Draw a figure of single transistor dynamic memory cell.
4. Draw the circuit of weighted resistor type DAC.
5. List different types of A/D converter.
6. Draw the logic flow chart of successive approximation converter.
7. Classify ROM drawing necessary diagram.
8. List and explain disadvantages of weighted resistors type DAC.

Q.3 Write a note on RAM, ROM & PROM.

[08]

OR

Q.3(a) Explain in detail ROM organization drawing ROM block diagram.

[04]

Q.3(b) Explain ROM timing diagram in detail.

[04]

Q.4 Give an account of MASK Programmed ROM.

[08]

OR

Q.4 Give an account of Tristate-Switch.

[08]

Q.5 Draw the circuit of R-2R ladder type D/A converter, explain its working in detail.

[08]

OR

Q.5 Give an account of counter type A/D converter.

[08]

Q.6 Give an account of V/F type A/D converter.

[08]

OR

Q.6 Draw a neat diagram of ADC 0801 IC and explain importance of each pin.

[08]