

VITHALBHAI PATEL & RAJRATNA P.T. PATEL SCIENCE COLLEGE
VALLABH VIDHYA NAGAR
INTERNAL TEST

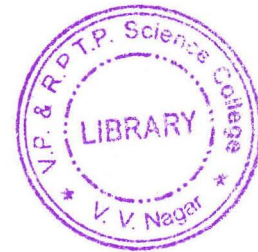
SY B.Sc. SEM: III
SUB: ELECTRONICS
SUB CODE: US03CELE01

DATE: 10th OCT 2014
Day: Friday
TIME: 2:00 pm to 3:30 pm
TOTAL MARKS: 25

Q. 1 Choose the correct answer.

[03]

- (1) Reactance of Inductor $X_L =$ _____.
- (A) $2\pi fL$ (C) $2\pi fC$
(B) $1/2\pi fC$ (D) None of above
- (2) Doped Semiconductor material is termed as _____.
- (A) Intrinsic (C) Insulator
(B) Extrinsic (D) None of above
- (3) The output of a voltage doubler is a _____ voltage.
- (A) AC (C) Zero
(B) DC (D) None of above



Q.2 Answer the following in short.(Attempt Two)

[04]

- (1) Write a note on wire-wound Resistor.
(2) Explain the time constant of RL circuit drawing necessary diagram.
(3) Define modulation.

Q.3 (A) List different types of fixed resistors & Explain any two in detail.

[06]

OR

Q.3 (A) List different types of fixed Capacitor & Explain any two in detail.

[06]

Q.4 (A) Give an account of step voltage Response of RL-circuit.

[06]

OR

Q.4 (A) Explain in detail Forward biased PN-junction diode.

[03]

(B) Explain in detail Reverse biased PN-junction diode.

[03]

Q.5 (A) Explain working of any two of the following drawing necessary circuit diagram.

[06]

- (1) Peak-Rectifier
(2) The Diode Clamper
(3) Voltage Doubler

OR

Q.5 (A) Explain Amplitude Modulation drawing necessary diagram & derive necessary derivations.

[06]

Extra